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Application No. 10/748,339

AMENDMENTS TO THE CLAIMS:

1. (Original) A decoder for temporarily storing in a buffer memory in sector units digital data having a predetermined number of bytes processing the digital data by correcting and detecting code errors included in the digital data, and transferring the processed digital data, the decoder comprising:

a check head register for storing a first address of the buffer memory when the storing of the processed digital data to the buffer memory is started;

a check sector counter for counting the number of sectors of the processed digital data stored in the buffer memory to generate a count value;

a command decision circuit connected to the check head register and the check sector counter for deciding whether the digital data requested to be transferred is stored in the buffer memory based on the first address, the count value, and a head address of the digital data requested to be transferred; and

a command register connected to the command decision circuit for storing a data transfer request command, wherein the command decision circuit permits the decoder to transfer the processed digital data when deciding that the digital data requested to be transferred is stored in the buffer memory.

2. (Original) The decoder according to claim 1, wherein the count value corresponds to an address interval of the processed digital data occupying the buffer memory, and wherein the command decision circuit adds the address interval to the first address to generate a comparison address and decides whether the digital data requested to be transferred is stored in the buffer memory by comparing the comparison address and the head address of the digital data that is requested to be transferred.

3. (Original) The decoder according to claim 2, wherein the command register stores the data transfer request command that includes address information, wherein the command decision circuit

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decides the head address of the digital data requested to be transferred based on the address information.

4. (Original) The decoder according to claim 2, wherein the command decision circuit decides the head address of the digital data that is requested to be transferred based on address information received from an external microcomputer.

5. (Original) The decoder according to claim 1, wherein the decoder is a CD-ROM decoder.

6. (Original) The decoder according to claim 1, wherein the decoder is a DVD decoder.